DECODER

Patent Number:

JP6133243

Publication date:

1994-05-13

Inventor(s):

SENGOKU YOSHIYA; others: 02

Applicant(s):

SONY CORP

Requested Patent:

☐ <u>JP6133243</u>

Application Number: JP19920305942 19921021

Priority Number(s):

IPC Classification:

H04N5/445; H04N5/265; H04N7/08; H04N7/20; H04N17/00

EC Classification:

Equivalents:

Abstract

PURPOSE: To recognize the operating state of the output mode of the decoder without changeover of the input mode at the monitor side by latching a current output signal for a prescribed time when an output signal is selected and outputting operation information or the like while being superimposed on the output

CONSTITUTION: When the user enters, e.g. an input signal changeover key to select an HD signal from a video signal during selection at present, for example, an OSD information output switch SW5 and a BLK switch SW6 set at the position of the video signal side at present are held for a prescribed time. Then a message of a content in the video signal is superimposed onto an output signal in terms of OSD information and the resulting signal is outputted to a monitor via a video output terminal t3. The OSD information is received from an input terminal t3r of the monitor and outputted to a CRT for a prescribed time when the OSD output signal of the decoder is latched via the input changeover switch. When a prescribed time elapses, the switches SW5, SW6 are thrown to the position of HD and the OSD information is outputted while being superimposed on the HD signal.

Data supplied from the esp@cenet database - I2

APX 312990